

J-2/2111

10083/NB

Computer System Organisation and Architecture -213
Maximum Marks: 75 SEM -IV Duration: 3 hrs.

Note: Candidates are requested to attempt two questions each from Section A and B carrying 15 marks each. Section C is compulsory consisting of 5 short answer-type questions carrying 3 marks each.

SECTION-A

1. What do you mean by computer system organization? Explain CPU organization in detail.
2. What is meaning of instruction? Explain instruction cycle in detail.
3. Define cache memory? Discuss various memory mapping procedures in detail.
4. Define the term interrupt? Explain various types of interrupt.

SECTION-B

5. Explain in detail various Logic micro-operations with suitable example.
6. Briefly discuss the following
 - a. Bus and Memory transfer
 - b. Control word
 - c. Control memory
7. Explain in detail various I/O interface in detail with suitable examples.
8. What do you mean by data transfer? Explain Strobe control and handshaking instructions

SECTION-C

9. Write short note on the following
 - i. Instruction level parallelism
 - ii. Byte ordering
 - iii. Cache memory
 - iv. Shift micro operation
 - v. Register transfer